SOI platform for high speed all optical wavelength conversion

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Abstract—In this paper we present new design of an all-optical wavelength converter (AOWC) based on silicon-on-insulator platform. For that purpose we combine nonlinearities of semiconductor optical amplifiers (SOA) with high finesse Mach-Zehnder filters, available in SOI-technology.

I. INTRODUCTION

A review of recent research reveals that SOAs are a widely used tool in optical wavelength conversion, taking advantage of their nonlinearity \cite{1, 2, 3}. There is a high integration potential of SOAs, they have high power efficiency, and, above all, the potential for ultrafast wavelength converters (>100Gbit/s). The European project “BOOM” pursues the realization of Terabit router functionality utilizing ultrafast optical signal processing in SOAs. As part of the project, the project team works on the development of an integrated wavelength converter. The converter benefits from the combination of high speed SOAs and an underlying SOI-platform. The platform serves as a motherboard for SOA integration, and provides high quality passive optical filters. Here, the passive filter consists of two cascaded Mach-Zehnder delay interferometers (MZ-DI) with delays of 1 ps and 2 ps.

I. PRINCIPLE OF WAVELENGTH CONVERSION

When a fast optical pulse is applied, SOA devices show recovery times of the order of hundred picoseconds (see Fig.1). Nevertheless, high-speed wavelength converters up to 40 Gbit/s have been experimentally demonstrated \cite{4}.

In the present contribution high speed performance is achieved by using the chirped, wavelength converted light output of the SOA in combination with two MZ-DI.

Fig.1 SOA Gain recovery curve

The working principle is as follows: An optical pulse injected into an SOA first modulates the gain. Furthermore, there is change in refractive index. This leads to chirp of the output signal – red- or blue-shifted, depending on the edge of incoming pulse. It has been shown that filtering of the red-chirped part of that output light can be used to obtain non-inverted wavelength conversion \cite{5}.

Similarly, the filtering of the blue-chirped part of output light can lead to an inverted wavelength conversion.

According to simulations, a well adjusted band pass (i.e. a well adjusted frequency transfer function at the output of the SOA) can decrease the recovery time from 90 ps to less than 3ps. In detail, various processes occur: Before saturation of the SOA, the red-chirped gain undergoes higher attenuation by the adjusted filter. This results in a reduced transmittance of incoming probe light. If the SOA starts to recover, the blue-shifted light (smaller wavelength) passes the filter, this time with higher transmission (Fig. 2). Therefore, gain saturation is balanced in case of the blue-shifted signal, so that we can achieve an increased signal level before the recovery cycle of the SOA is completed.
In our design we use 2x2 multi-mode interference (MMI) couplers with well known power and phase relations. For a single MZ-DI, the power transmission (T) at output as a function of frequency is:

\[ T_1 = \frac{P_O}{P_{in}} \cdot \sin^2(\Delta\phi/2) \]

\[ T_2 = \frac{P_O}{P_{in}} \cdot \cos^2(\Delta\phi/2) \]

The phase difference \( \Delta\phi \) is defined with propagation constant \( \beta \) of the waveguides and the adjusted time delay in the interferometer given by a length difference \( \Delta L \). For the complete device with cascaded delay interferometer we obtain transmission characteristic for output 3 and 4:

\[ T_3 = \frac{P_O}{P_{in}} \cdot \sin^2(\Delta\phi_1/2) \cdot \sin^2(\Delta\phi_2/2) \]

\[ T_4 = \frac{P_O}{P_{in}} \cdot \sin^2(\Delta\phi_1/2) \cdot \cos^2(\Delta\phi_2/2) \]

For case of half FSR (equal to half of the length difference) of the first MZ-DI in comparison to the second MZ-DI, we can substitute \( \Delta\phi_1/2 \) by \( \Delta\phi_2/4 \). For better understanding of the complete interferometer structure, we also split up the structure into two single devices with different FSR corresponding to delay of 1 ps and 2 ps. The designed chip area for a couple of single and combined structures was 10 mm x 25 mm.

III. EXPERIMENTAL RESULTS

For fabrication of designed MZ-structures we used SOI rib waveguide technology. We used substrates with top silicon thickness of 4 μm, which is very convenient in terms of packaging because it enables high coupling efficiency (~0.5 dB loss per facet) and reduced sensitivity to fiber misalignment. For rib definition we used standard contact lithography and reactive ion etching.

Fig. 5 shows measured filter curve for bar and cross port of structure with 2 ps time delay. Both outputs show for TE and TM polarization good homogeneity other C-band with extinction ratios up to 30 dB. The maximum loss of structure in reference to back-to-back (B2B) signal is 4.2 dB. The B2B-reference in that context respects all loss given by fiber-to-chip coupling, additional connections and loss of the passive waveguide network. Fig. 6 shows the same measurement for single MZ-DI device with 1 ps. Polarization dependent frequency shift (PDFS) is always an issue in planar waveguide technologies.
Consistent results can achieve the expected performance.

**Fig. 5** Filter characteristic of delay interferometer with 2 ps time delay for TE (red line) and TM-mode (blue line). Solid line corresponds to bar port; dashed line to cross port.

![Filter characteristic](image)

**Fig. 6** Filter curves of delay interferometer with 1 ps time delay.

With a 2-cladding technology we could recently achieve relatively low PDFS, i.e., absolute birefringence, for the 2-ps MZ-DI. The amount of birefringence caused by rib waveguide geometry is in the range of $3 \times 10^{-5}$. With applied cladding, under conservation of performance, we could reduce absolute birefringence to $4 \times 10^{-5}$ (~ 2 GHz PDFS).

![Filter characteristic](image)

**Fig. 7** Filter characteristic of cascaded Mach-Zehnder delay interferometers with 1 and 2 ps time delay (TE-mode). Solid lines correspond to bar port (a); dashed line to cross port (b).

Results for cascaded devices are shown in Fig. 7. Shape of the filter characteristic for both outputs is consistent with calculated output signals. Extinction ratios are comparable to the results of single devices. The loss is slightly increased due to the doubling of involved interference couplers. As measured results show, cascaded SOI-MZI structure delivers required performance to act as proposed filter in connection with SOA. Verification experiments with discrete SOA and SOI-DIs are planned in the near future. Eventually, SOA and SOI-DIs will be integrated on a single chip. First integration tests of SOAs on SOI have been conducted.

**Fig. 8** Flip-chip soldered SOA on SOI motherboard.

![Flip-chip soldered SOA on SOI motherboard](image)

**IV. SUMMARY**

The concept for a new design of an all-optical wavelength converter (AOWC) based on silicon-on-insulator platform has been provided. Realized SOI-filters show expected filter transfer functionality with high extinction ratios (up to 30 dB) and low insertion loss. First flip-chip tests have been conducted. High speed $\lambda$-conversion experiments with SOI chips and SOAs are underway.

**V. ACKNOWLEDGEMENTS**

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**VI. REFERENCES**


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